



IN THE CLAIMS

Misnumbered claims 51- 79 and 81 - 105 have been amended as requested in the Office Action to claims 36-89 as indicated below.

Please amend the following claims:

A "clean copy of the amended claims are provided below:"

36. A machine-readable medium having stored thereon instructions, which if executed by a machine, cause said machine to perform operations comprising:
- determining an amount of time a processor is in a first power consumption state, said amount of time said processor is in said first power consumption state comprising a period of time in which a clock of said processor is stopped;
 - reducing a voltage level applied to said processor in response to said amount of time said processor is in said first power consumption state.
37. The machine-readable medium of Claim 36 wherein reducing said voltage level applied to said processor is performed in response to said amount of time exceeding a selected amount of time.
38. The machine-readable medium of Claim 37 wherein determining comprises reading a timer to determine said amount of time said processor is in said first power consumption state.
39. The machine-readable medium of Claim 38 wherein said reducing said voltage level comprises placing said processor in a power-off state.

40. The machine-readable medium of Claim 36 wherein determining the amount of time said processor is in the first power consumption state comprises monitoring an activity level demand within a computer system.

41. The machine-readable medium of Claim 40 wherein reducing the voltage level applied to the processor comprises operating said processor in a reduced power consumption state while satisfying said activity level demand.

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42. A machine-readable medium having stored thereon instructions, which if executed by a machine, cause said machine to perform operations comprising:
determining an amount of time a clock of a processor is stopped;
placing said processor into a reduced power consumption state in response to said amount of time said clock of said processor is stopped.

43. The machine-readable medium of Claim 42 wherein said placing comprises reducing a voltage of said processor.

44. The machine-readable medium of Claim 43 wherein said determining an amount of time said clock of said processor is stopped contributes to determining a system idle time.

45. The machine-readable medium of Claim 44 wherein said clock of said processor

is stopped when the processor is in a sleep state.

46. The machine-readable medium of Claim 45 wherein a timer is read to determine said amount of time said processor is in said first power consumption state.

47. The machine-readable medium of Claim 46 wherein said reduced power consumption state is a power-off state.

48. The machine-readable medium of Claim 43 wherein reducing said voltage of said processor is performed in response to said amount of time said clock of said processor is stopped exceeding a selected amount of time.

49. An apparatus comprising:
a power management module to determine an amount of time a processor is in a first power consumption state, said first power consumption state comprising a period in which said processor is stopped;

a power reduction module to place said processor into a reduced power consumption state in response to said amount of time said processor is in said first power consumption state.

50. The apparatus of Claim 49 wherein said power reduction module comprises a software routine.

51. The apparatus of Claim 49 wherein said power management module comprises a timer.

52. The apparatus of Claim 50 wherein said power management module comprises a timer.

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53. The apparatus of Claim 52 wherein said power reduction module is enabled to reduce a voltage applied to said processor.

54. The apparatus of Claim 53 wherein said power reduction module is enabled to reduce said voltage in response to said amount of time exceeding a selected amount of time.

55. The apparatus of Claim 54 wherein said power management module comprises a software routine.

56. The apparatus of Claim 55 wherein said first power consumption state is a sleep state.

57. The apparatus of Claim 56 wherein said reduced power consumption state is a power-off state.

58. An apparatus comprising:

a power management module to determine an amount of time a processor is in a first power consumption state, said first power consumption state comprising a period in which said processor is stopped;

a power reduction module to place said processor into a reduced power consumption state in response to said amount of time said processor is in said first power consumption state, said power reduction module comprising a software routine, said power management module comprising a timer.

59. A system comprising:

a memory;

a processor coupled to said memory;

a power management module to detect an amount of time said processor is in a first power consumption state, said first power consumption state comprising a period of time in which a clock of said processor is stopped, said processor being placed into a reduced power consumption state in response to said amount of time said processor is in said first power consumption state.

60. The system of Claim 59 wherein said reduced power consumption state comprises a reduced voltage state of said processor.

61. The system of Claim 60 wherein said reduced voltage state comprises a power-off state.

62. The system of Claim 61 wherein said power management module is enabled to determine a system idle time.

63. The system of Claim 62 wherein said system idle time is represented by said amount of time said processor is in said first power consumption state.

B1 64. The system of Claim 63 wherein said first power consumption state is a sleep state.

65. The system of Claim 59 wherein said power management module comprises a software routine.

66. The system of Claim 59 wherein said power management module further comprises a timer.

67. The system of Claim 65 wherein said power management module further comprises a timer.

68. The system of Claim 59 further comprising a configurable device;
power management software to power manage said configurable device.

69. The system of claim 68 further comprising:

power management software to cooperate with said device manager to allow power management of a plurality of devices in the system which are configurable devices, and to manage a power level for each of the plurality of devices in the system. the power management software being capable of placing one or more of said plurality of devices in a reduced power consumption state.

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70. The system of claim 69 further comprising a plug and play manager.

71. The system of claim 70 wherein said power management software is to communicate with said plug and play manager to update data structures if configuration changes occur to allow power management of dynamically reconfigurable devices.

72. The system of claim 71 wherein said power management software registers with said device manager to be notified of configuration changes.

73. The system of claim 72 wherein said power management software is to provide system level power management including the use of multiple system level power management states for said system, and to provide multiple power management states for said plurality of devices.

74. The system of claim 72 wherein said power management software is to provide support for idle detection for at least one of said plurality of devices.

75. The system of claim 72 wherein said power management software is to place the system in a sleep state when the system is idle and to keep said system in said sleep state until activity is detected, and wherein the sleep state is one of a plurality of system power management states, and further wherein said system stops a clock for a system processor in said sleep state.

76. A method comprising:

determining an amount of time a clock of a processor is stopped;

placing said processor into a reduced power consumption state in response to said amount of time said clock of said processor is stopped.

77. The method of Claim 76 wherein said placing comprises reducing a voltage of said processor.

78. The method of Claim 77 wherein said determining said amount of time said clock of said processor is stopped contributes to determining a system idle time.

79. The method of Claim 78 wherein said clock of said processor is stopped when the processor is in a sleep state.

80. The method of Claim 79 wherein a timer is read to determine said amount of time said processor is in a first power consumption state.

81. The method of Claim 80 wherein said reduced power consumption state is a power-off state.

82. The method of Claim 81 wherein reducing said voltage of said processor is performed in response to said amount of time said clock of said processor is stopped exceeding a selected amount of time.

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83. The method of claim 82 further comprising power managing in cooperation with a device manager a plurality of devices in a system which are configurable devices; and managing a power level for each of the plurality of devices in the system, the power managing comprising placing one or more of said plurality of devices in a reduced power consumption state.

84. The method of claim 83 wherein said system comprises a plug and play manager.

85. The method of claim 84 wherein said power managing comprises communicating with said plug and play manager to update data structures if configuration changes occur to allow power management of dynamically reconfigurable devices.

86. The method of claim 85 further comprising registering power management software with said device manager to be notified of configuration changes.

87. The system of claim 86 wherein said power managing comprises providing system level power management including the use of multiple system level power management states for said system, and to provide multiple power management states for said plurality of devices.

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88. The method of claim 86 wherein said power managing comprises supporting idle detection for at least one of said plurality of devices.

89. The method of claim 86 wherein said power managing comprises placing the system in a sleep state when the system is idle and to keep said system in said sleep state until activity is detected, and wherein the sleep state is one of a plurality of system power management states, and further wherein said system stops a clock for a system processor in said sleep state.